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INFORMATION DISCLOSURE STATEMENT BY APPLICANT  (Use several sheets if necessary)			APPLICANT HONMURA, Tetsuroo		
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**U.S. PATENT DOCUMENTS**

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AP							
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AR							
AS							
AT							

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**

TQN	AU	M. Morris Mano, "Logic and Computer Design Fundamental", 2000, Prentice Hall, Chapter 11/Input-Output and Communication, 11-3/I/O Interfaces, Pgs. 580-609
	AV	
	AW	
	AX	
	AY	
	AZ	

Examiner <i>Hyunyeong Jeon</i>	Date Considered 04/13/2005
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